



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Intel Patent Application of:

Wijeratne, Sapumal

Application No.: 10/769,257

Filed: January 30, 2004

For: A Single Stage, Level Restore
Circuit With Mixed Signal Inputs

Examiner: Vibol Tan

Art Unit: 2819

Confirmation No. 8142

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on the date below:

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Signature: Heather L. Adamson Date: 04/27/2005

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

INTRODUCTORY COMMENTS

Dear Sir:

This communication is submitted in response to the Office Action mailed **April 13, 2005** (herein "Office Action"). Reconsideration of the above captioned application in view of the amendments and remarks to follow is respectfully requested.

Amendments to the Drawings begin on page 2 of this paper and include an attached replacement sheet.

Remarks begin on page 3 of this paper.

An Appendix including amended drawing figures is attached following page 3 of this paper.